

SENIOR DIVISION SOLUTIONS

<p>1. Graph Theory</p> <p>To find the number of paths of length 3 from, each vertex, add the entries in the cubed adjacency matrix in each row. B has the most at 32.</p> $\begin{vmatrix} 0 & 1 & 0 & 1 & 1 \\ 1 & 1 & 1 & 0 & 1 \\ 0 & 0 & 0 & 1 & 1 \\ 1 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 & 0 \end{vmatrix}^3 = \begin{vmatrix} 5 & 8 & 3 & 6 & 6 \\ 6 & 10 & 3 & 8 & 5 \\ 4 & 5 & 2 & 3 & 3 \\ 5 & 9 & 3 & 7 & 6 \\ 3 & 6 & 2 & 5 & 5 \end{vmatrix}$	1. B 32
<p>2. Graph Theory</p> <p>The 13 cycles from B are: BAB, BAFB, BCB, BCDEB, BCDEAB, BCDEAFB, BCDEFB, BCFB, BDEB, BDEAB, BDEAFB, BDEFB, BFB</p>	2. 13
<p>3. Digital Electronics</p> <p>The circuit translates to: $\overline{\overline{A(A+B)}(BC+\overline{C})}$</p> $\overline{\overline{A(A+B)}(BC+\overline{C})} = \overline{\overline{A(A+B)} + \overline{BC+\overline{C}}} = \overline{\overline{A} + \overline{A+B} + BC + \overline{C}}$ $= A + A + B + BC + \overline{C} = A + B(1+C) + \overline{C} = A + B + \overline{C}$ <p>So $A + B + \overline{C} = 0$ implies $A = 0 \wedge B = 0 \wedge \overline{C} = 0$. (0, 0, 1) makes it FALSE.</p>	3. (0, 0, 1)
<p>4. Digital Electronics</p> <p>The circuit translates to: $A(\overline{A+B} \oplus \overline{BC}) + \overline{C}$</p> $\begin{aligned} A(\overline{A+B} \oplus \overline{BC}) + \overline{C} &= A(\overline{\overline{A+B}} + \overline{\overline{BC}}) + \overline{C} \\ &= A((A+B)\overline{BC} + \overline{AB}(\overline{B+C})) + \overline{C} \\ &= A(AB\overline{C} + B\overline{C} + \overline{AB} + \overline{ABC}) + \overline{C} \\ &= AB\overline{C} + ABC\overline{C} + A\overline{AB} + A\overline{ABC} + \overline{C} \\ &= AB\overline{C} + \overline{C} = \overline{C}(AB+1) = \overline{C} \end{aligned}$	4. \overline{C}
<p>5. Assembly Language</p> <p>This program converts 7325_8 to a base 10 number.</p>	5. 3797